

Overview of Open-Source EDA-Tools and PDKs

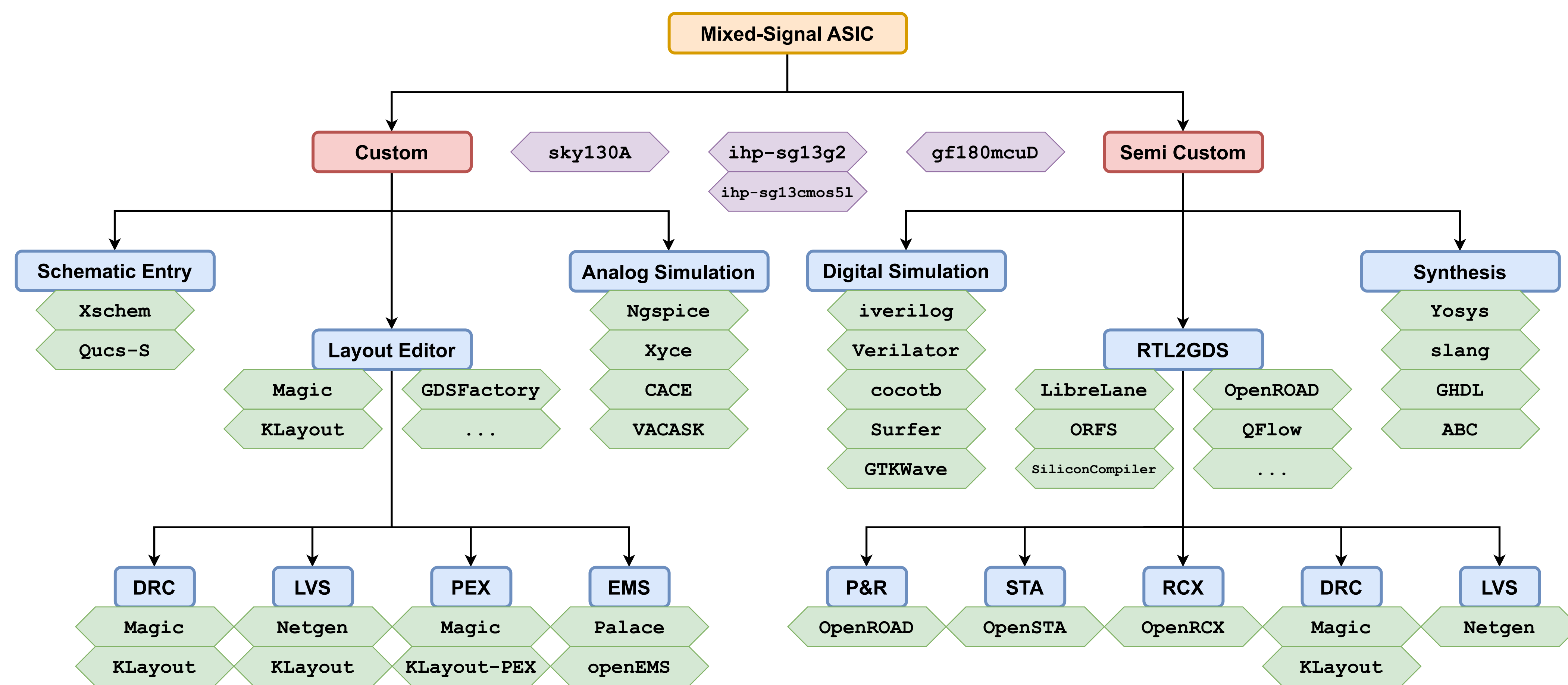


Figure 1. Overview of the most important tools in the IIC-OSIC-TOOLS Docker image [1].

Examples of Open-Source Chips

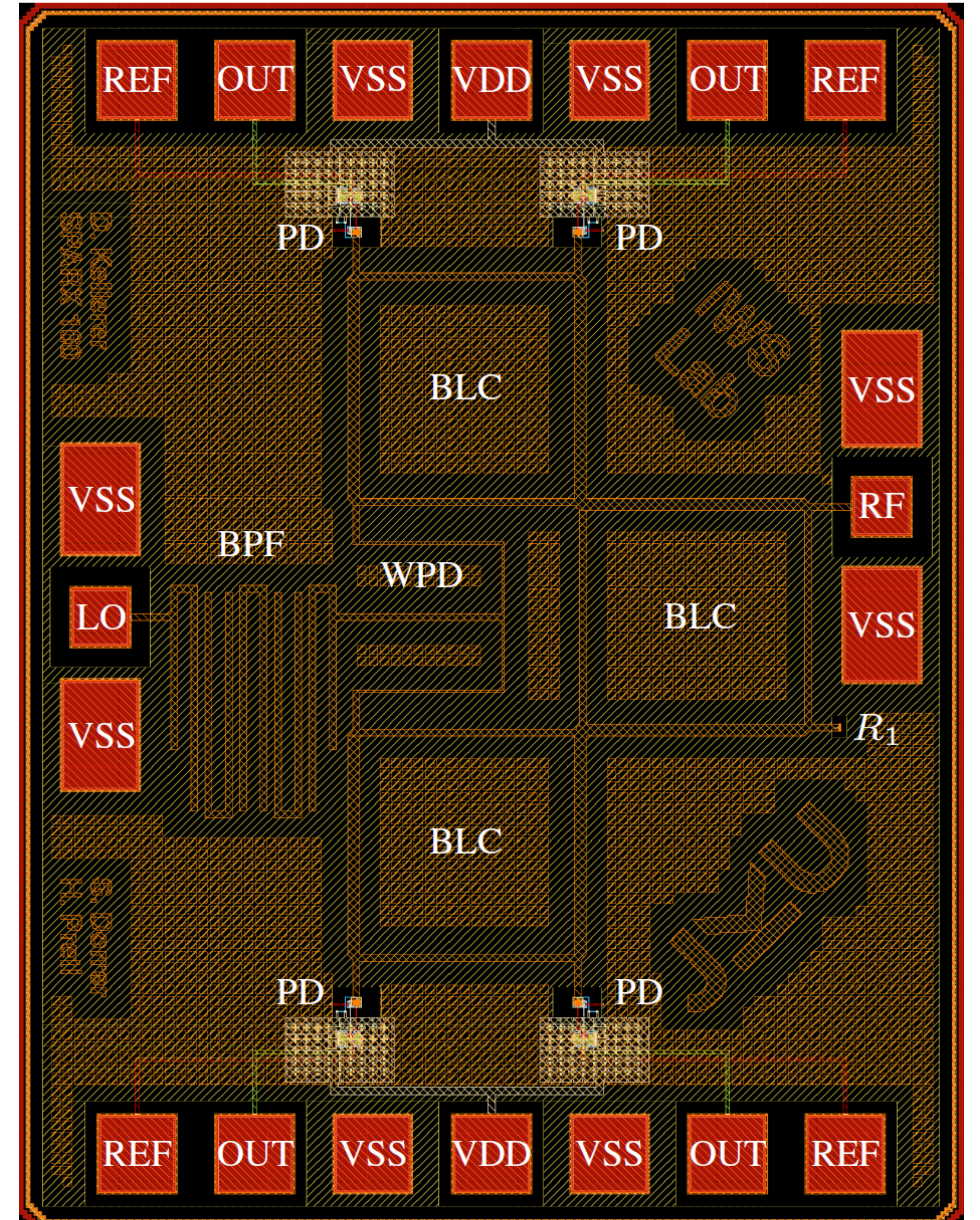
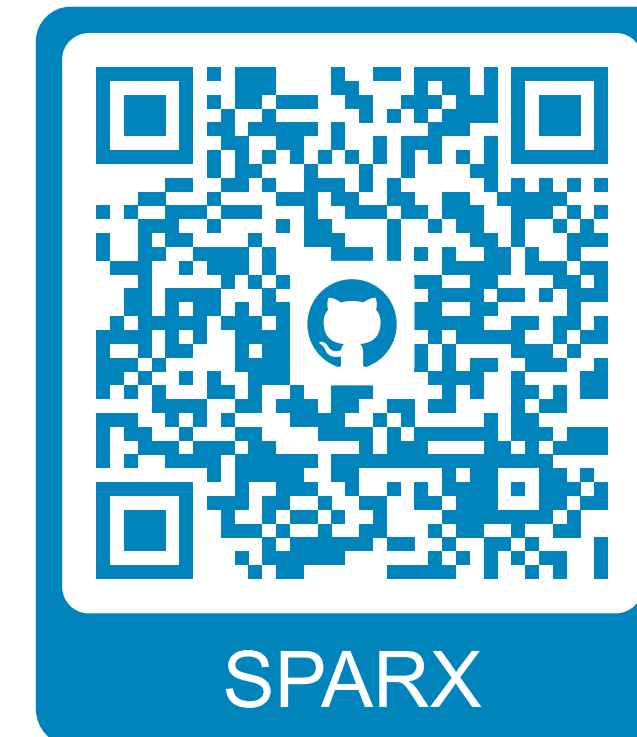
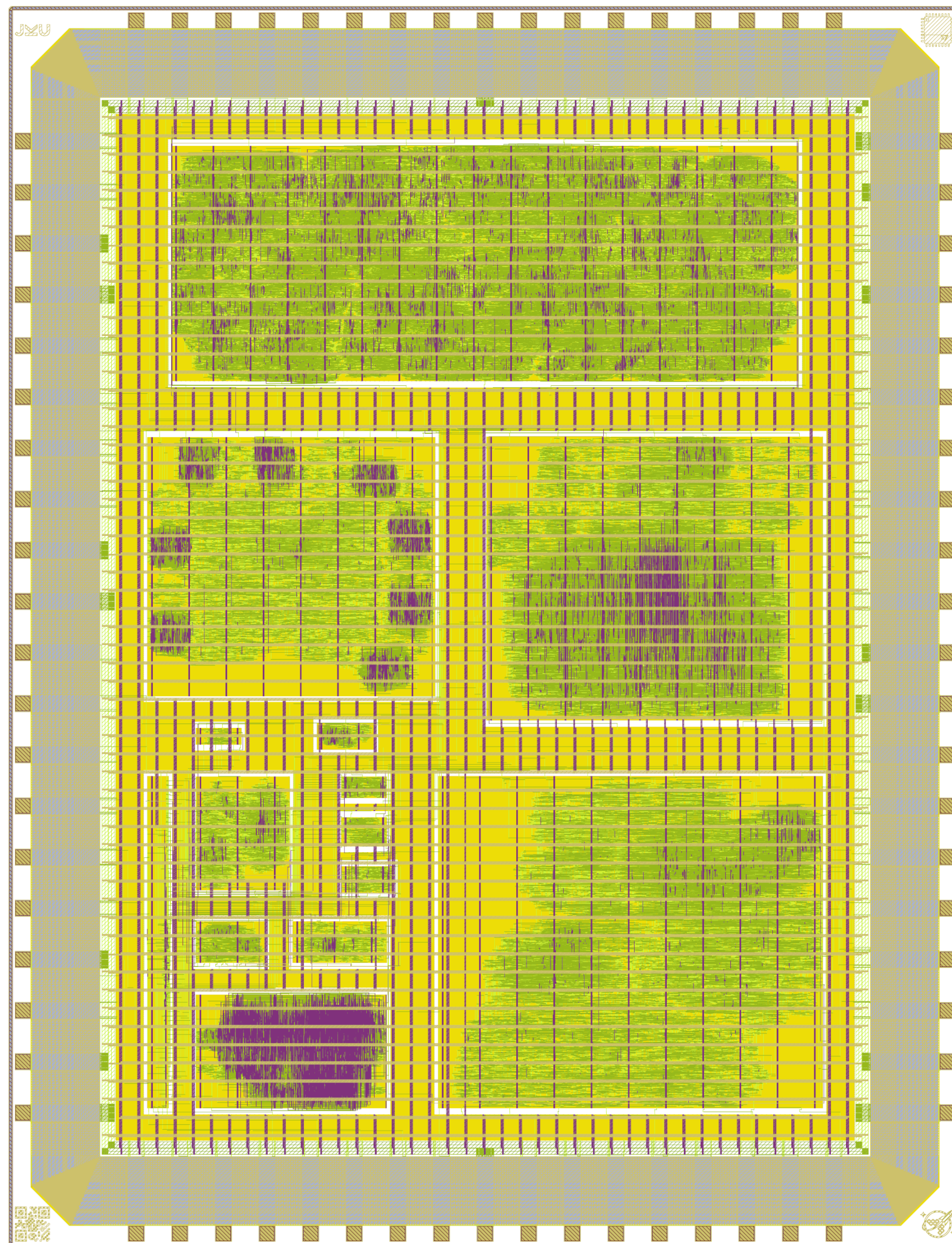


Figure 2. Render of the gf180mcuD multi-project chip (4 mm x 5 mm) [2].

Figure 4. Render of the ihp-sg13g2 SPARX RFIC (1 mm x 1.4 mm) [4].

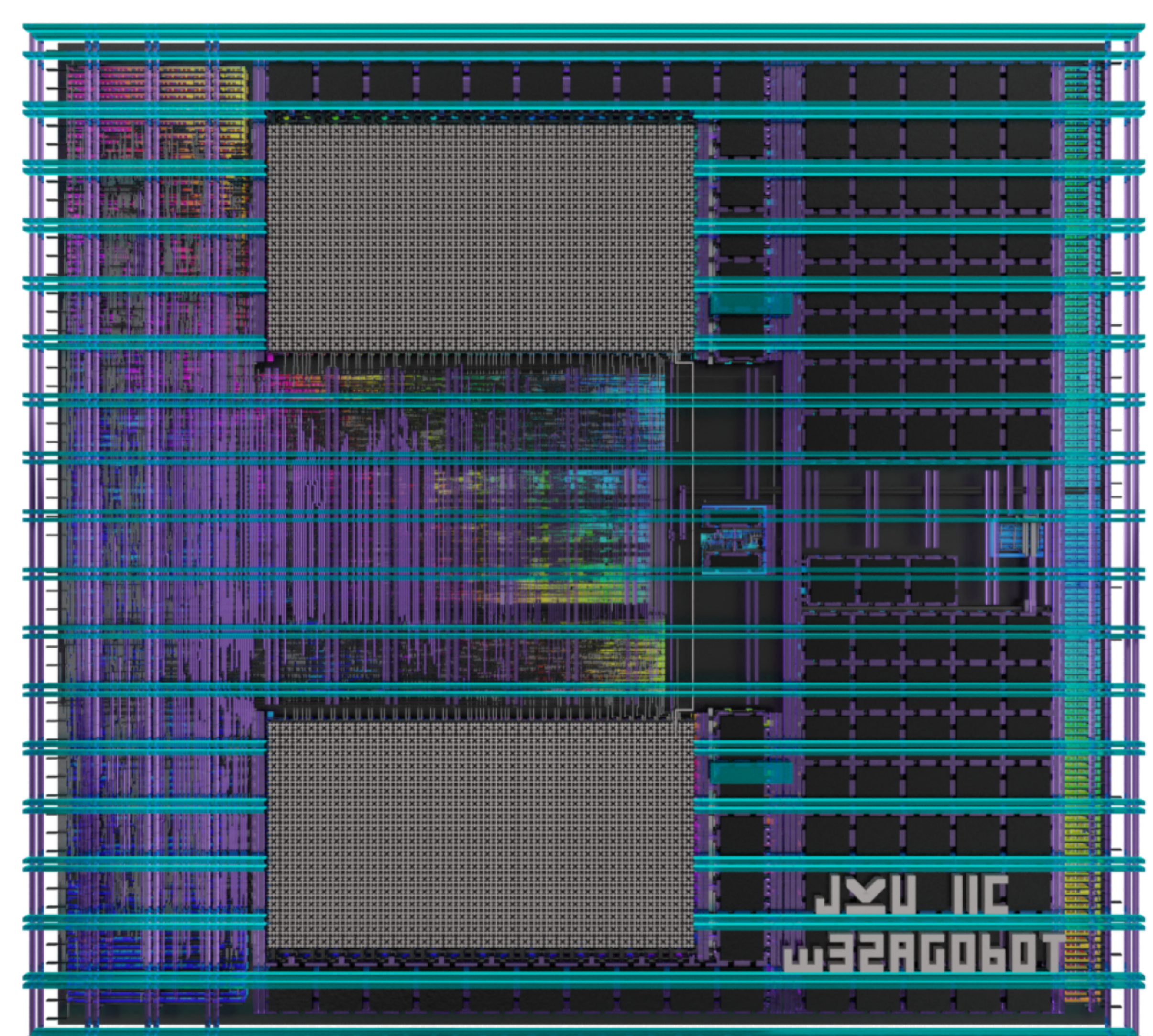
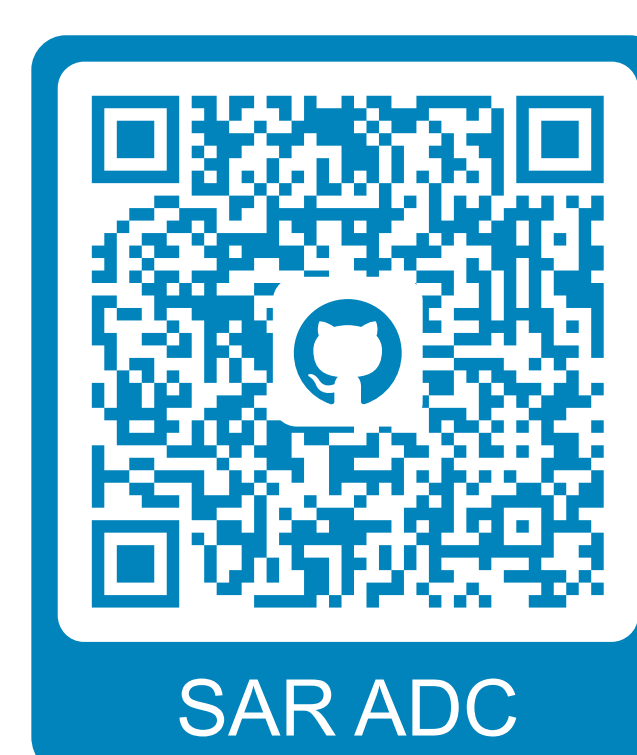
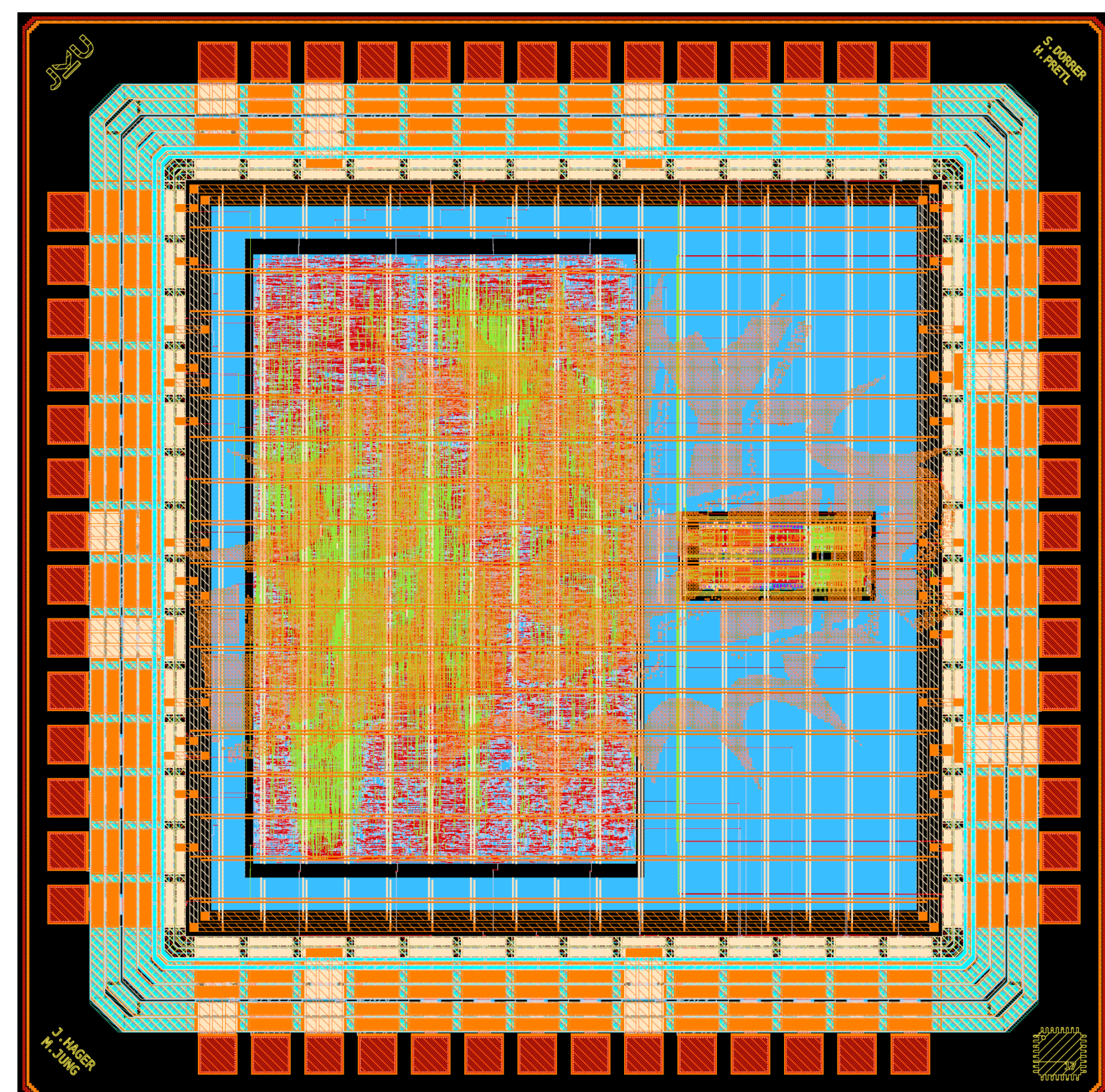


Figure 3. Render of the ihp-sg13g2 TinyWhisper SoC (2 mm x 2 mm) [3].

Figure 5. Render of the sky130A 12-bit SAR ADC layout (400 μm x 400 μm) [5].

[1] H. Pretl et al., *GitHub Repository for the IIC-OSIC-TOOLS*, Sep. 2023.[2] S. Dorrer et al., *GitHub Repository for a waferspace MPW Multi-Project Chip by JKU*, Dec. 2025.[3] S. Dorrer et al., *GitHub Repository for TinyWhisper: An Open-Source Fully-Integrated Multi-Mode Short-Wave Transmitter for Amateur Radio Applications in 130-nm CMOS*, Apr. 2026.[4] S. Dorrer et al., *GitHub Repository for SPARX: An Open-Source, Automated, Programmatically Generated, Frequency-Scalable Six-Port Receiver in 130-nm CMOS*, Apr. 2026.[5] M. Moser et al., *GitHub Repository for an Open-Source 12-bit Non-Binary SAR-ADC*, Sep. 2023.